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		form 1449A/	то		Complete if Known	
A. INIE	EMATIOI	N DISC	OSLIDE	Application No.	09/888,296	
	TOMENT I	DV ADI	LOSURE	Filing Date	June 22, 2001	
SIAI	CMENI	BT AP	PLICANT	First Named Inventor	David A. Fotland	
				Art Unit	2183	
				Examiner Name	Tonia Meonske	
Sheet	1	of	1	Attorney Docket Number	20880-06029	

			U.S. PAT	ENT DOCUMENTS
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Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
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Examiner Signature		enial n	Jeonske	Date Considered	Ju	me 23,2006
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

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¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

PTO/SB/08A (10-01)

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	Substitute for	r form 1449/	VPTO		Complete if Known	
INEO	DMATIO	N DIS	CLOSURE	Application No.	09/888,296	
		_	PLICANT	Filing Date	June 22, 2001	
SIA	I CIAICIA I	DIA	PLICANI	First Named Inventor	David A. Fotland	
				Art Unit	2183	
				Examiner Name	Charles A. Harkness	
Sheet	1	of	1	Attorney Docket Number	20880-06029	

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Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T⁰
C1	EL-KHARASHI, et al., Multithreaded Processors: The Upcoming Generation for Multimedia Chips, 1998 IEEE Symposium on Advances in Digital Filtering and Signal Processing, June 5-6, 1998, pp. 111-115	
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